

Department of Computer Science & Engineering
School of Engineering and Technology
Central University of Haryana, Mahendergarh

Announcement for PhD Admissions under "Visvesvaraya PhD scheme for Electronics & IT- Phase II " Sponsored by DeitY, MCIT, Govt. of India

Department of Electronics and Information Technology (DeitY), MCIT, Govt. of India has granted few scholarships under the scheme titled "Visvesvaraya PhD Scheme for Electronics & IT- Phase II". The major features of the scheme for the full-time scholars are given below:

Financial Support for Full-time PhD candidates:

- Fellowship: Rs. 38,750/- per month in 1st & 2nd year and @Rs. 43,750/- per month in 3rd, 4th and 5th years of PhD. (support till PhD completion or 5 years whichever is earlier).
- Reimbursement of Rent (RoR): This component is linked with the fellowship of PhD Candidate. The rate of RoR will be provided as per norms. The classification of the cities is as per the notification issued by the Ministry of Finance for the reimbursement of HRA. HRA is available to candidates who do not reside in the campus as per government rules.
- Annual contingency will also be provided.

Eligibility Criterion: Qualification and admission procedure will be the same as that of regular PhD admission of Central University of Haryana. For the last date of submission of applications and other details, please visit the following website <http://phd.cuh.ac.in/>.

How to Apply?

It is not necessary to submit a separate application for this fellowship. Those who are applying for the PhD admission may opt for the fellowship. If you want to be considered for this fellowship bring a one page writeup indicating your interest in this fellowship at the time of interview. Please note that your original PhD application will still be valid even if you are not considered for this fellowship.

HOD CSE